



## Supplementary Course (EVA) at ZHAW School of Engineering

## Title:Realtime DAQ and Control using LabVIEWShort Code:rEVA\_LabVIEW

ECTS Credits	3			
Profile	Mechatronics & Automation (MA)			
Responsible Institute /Centre	Institute of Mechatronic Systems (IMS)			
Responsible lecturer and contact informtion	Otto Fluder (fldr)			
Type and duration of examinations	<ul> <li>2 Projects (50%):</li> <li>Synchronous data acquisition with DAQmx</li> <li>FOC control for a stepper-motor with FPGA data acquisition on myRIO</li> <li>60min written Exam (50%)</li> </ul>			
Start date and duration	Semester: Autumn Detail: Calendar week 38 - 6			
Location	Winterthur			
Course type	<ul> <li>Weekly, semester rhythm</li> <li>Contact hours: 40 hrs (3 hrs/week lectures)</li> <li>Guided self-study: 20 hrs (2 projects)</li> <li>Independent self-study: 30 hrs (self paced online NI-courses)</li> </ul>			
Language of instruction	English			
Short description (max. 300 characters)	Students learn how to implement synchronous data acquisition using the graphical programming language LabVIEW. They learn all the fundamentals and details of LabVIEW from scratch (no prerequisites). With this basic knowledge they will be capable to extend this to a real time application with FPGA data acquisition and develop a control algorithm on a myRIO system – all three stages (Windows-host, RT, FPGA) programed in LabVIEW.			
Contents and Learning Objectives	LabVIEW Core 1/2: The first step in the LabVIEW learning path gives the student the chance to explore the LabVIEW environment and interactive analysis, dataflow programming, and common development techniques in a hands-on format. In this part, they will learn how to develop data acquisition, instrument control, data-logging, and measurement analysis applications. At the end, they will be able to create applications using the state machine design pattern to acquire, analyze, process, visualize, and store real-world data. LabVIEW Real-Time and FPGA: This section delivers hands-on training for prototyping deterministic measurement and control systems. At the end of this part, they will be able to			





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	design, develop, and prototype a real-time (RT) application that handles communication between the RT target and a host computer using the LabVIEW Real-Time Module and FPGA.				
	<u>Case Studies using LabVIEW on a myRIO-System:</u> They will implement several projects using standard DAQ-tools and different low-level protocols like SPI, I2C, UART to communicate in real-time with sensors. Finally, they develop an FPGA interface for encoder, PWM and triggered current measurement to drive a stepper motor in a FOC-feedback control application.				
	<ul> <li>LabVIEW Core 1:</li> <li>Create applications that use data acquisition (DAQ) devices</li> <li>Understand front panels, block diagrams, icons, and connector panes</li> <li>Create user interfaces with charts, graphs and buttons</li> <li>Use the programming structures and data types that exist in LabVIEW</li> <li>Use various editing and debugging techniques</li> <li>Create and save VIs for use as subVIs</li> <li>Display and log data</li> <li>Use the state machine design pattern in your applications</li> </ul>				
	<ul> <li>LabVIEW Core 2:</li> <li>Implement multiple parallel loops and transfer data between the loops</li> <li>Create an application that responds to user interface events</li> </ul>				
	<ul> <li>LabVIEW Real-Time 1:</li> <li>Determine if a real-time solution is appropriate for a given problem</li> <li>Choose and configure the RT target hardware for a given real-time application</li> <li>Implement a deterministic application</li> <li>Understand how to reduce the jitter in a real-time application</li> <li>Communicate between a host computer and RT target using network Communication</li> </ul>				
	<ul> <li>LabVIEW FPGA:</li> <li>Design and implement applications with reconfigurable hardware using FPGA</li> <li>Read and write digital and analog FPGA-I/O's</li> <li>Realize synchronous and precise timing like SPI-communication</li> <li>Implement scaling and signal analysis on a FPGA system</li> <li>Implement RT-host to FPGA-target communication</li> </ul>				
Prerequisites	Basic knowledge in feedback control				
Literature	<ul> <li>Online courses from NI (National Instruments)</li> <li>LabVIEW Core1:</li> <li><u>https://learn.ni.com/learn/learning-path/labview-core-1-english-2019</u></li> </ul>				





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	LabVIEW Core2:					
	<ul> <li><u>https://learn.ni.com/learn/learning-path/labview-core-2-2019-english</u></li> </ul>					
	LabVIEW DAQmx:					
	https://learn.ni.com/learn/learning-path/data-acquisition-using-ni-					
	daqmx-and-labview-english-1					
	<ul> <li>LabVIEW Real-Time1:</li> <li><u>https://learn.ni.com/learn/course/real-time-1/real-time-1/lesson-content?client=national-instruments</u></li> </ul>					
	LabVIEW FPGA:					
	<ul> <li>https://learn.ni.com/learn/course/labview-fpga/labview-fpga/lesson-</li> </ul>					
	<u>content?client=national-i</u>	<u>nstru</u>	<u>iments</u>			
Special requirements	none					
Offer for profiles	Aviation (Avi)	$\boxtimes$	Business Engineering (BE)	$\boxtimes$		
	Computer Science (CS)	$\boxtimes$	Data Science (DS)	$\boxtimes$		
	Electrical Engineering (EIE)	$\boxtimes$	Energy & Environment (EnEn)	$\boxtimes$		
	Mechanical Engineering (ME)	$\boxtimes$	Mechatronics & Automation (MA	$\boxtimes$		
	Medical Engineering (Med)	$\boxtimes$	Photonics and Laser Engineering	$\boxtimes$		
	Information and Cyber Security (ICS)		Civil Engineering (CE)	$\boxtimes$		